



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of Laurence B. Boucher, et al. Ser. No: 10/678,336
Filing Date: October 3, 2003 Examiner: Unknown
Atty. Docket No: ALA-008H GAU: Unknown
For: METHOD AND APPARATUS FOR DYNAMIC PACKET BATCHING
WITH A HIGH PERFORMANCE NETWORK INTERFACE

January 6, 2004

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Information Disclosure Statement per 37 C.F.R. §1.98

Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, applicants bring one hundred and forty-four documents listed on the enclosed ten-page form PTO-1449 to the attention of the Examiner in the above-identified application.

Citation of these documents shall not be construed as an admission that these documents are prior art with respect to the instant invention, a representation that a search has been made, or an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

Copies of the documents listed on the enclosed ten-page form PTO-1449 are not submitted because they were submitted in earlier applications (09/384,792 and 10/005,536) which are relied upon for an earlier filing date under 35 U.S.C. §120. Documents listed on pages one through six of the enclosed ten-page form PTO-1449 were submitted in Application Serial No. 09/384,792, and documents listed on pages seven through ten were submitted in Application Serial No. 10/005,536.

Respectfully submitted,



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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on January 6, 2004.

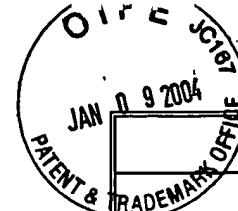
Date: 1-6-04



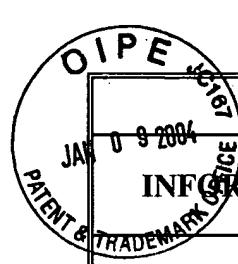
Mark Lauer

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT					Filing date: October 3, 2003			
					Inventors: Laurence B. Boucher, et al.			
					Group Art Unit: Unknown			
METHOD AND APPARATUS FOR DYNAMIC PACKET BATCHING WITH A HIGH PERFORMANCE NETWORK INTERFACE					Examiner name: Unknown			
					Attorney Docket No. ALA-008H			
U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate	
	A	4,336,538	12/28/92	Johnson et al.	364	200		
	B	5,097,442	03/17/92	Ward et al.	365	78		
	C	5,629,933	05/13/97	Delp et al.	370	411		
	D	5,664,114	09/02/97	Krech, Jr. et al.	395	200.64		
	E	5,794,061	08/11/98	Hansen et al.	395	800.01		
	F	5,802,580	09/01/98	McAlpice	711	149		
	G	5,815,646	09/29/98	Purcell et al.	395	163		
	H	5,771,349	06/23/98	Picazo, Jr. et al.	395	188.01		
	I	6,009,478	12/28/99	Panner, et al.	710	5		
	J	5,056,058	10/8/91	Hirata, et al.	364	900		
	K	5,913,028	6/15/99	Wang, et al.	395	200.33		
	L							
	M							
Foreign Patent Documents								
							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
	N	WO 99/65219 (PCT/US/99/13184)	6/10/99	International				
	O	WO 01/05107 A1 (PCT/US00/19006)	7/13/00	International				
	P	WO 01/05116A2 (PCT/US00/19243)	7/13/00	International				
	Q	WO 01/05123 A1 (PCT/US00/18976)	7/12/00	International				
	R	WO 01/04770A2 (PCT/US00/18939)	7/11/00	International				
	S	(WO 00/13091) PCT/US98/24943	11/20/98	International				
Examiner			Date Considered					
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.</p>								

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		Attorney Docket No. ALA-008H
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
	A	60/053,240 (U.S. Provisional Application), by Jolitz et al. (listed filing date July 18, 1997).
	B	Zilog Product Brief entitled "Z85C30 CMOS SCC Serial Communication Controller", Zilog Inc., 3 pages (1997).
	C	Internet web pages from "Adaptec.com" website directed to the Adaptec, AEA-7110C iSCSI Host Bus Adapter and about Adaptec's IP storage activities, 11 pages, downloaded and printed 10/1/01.
	D	Internet web pages from "iSCSIhba.com" website directed to JNI iSCSI HBAs including the "FCE-3210/6410", 6 pages, downloaded and printed 10/1/01.
	E	Internet web pages from the "iSCSI Storage.com" website that mention an Emulex HBA, 2 pages, downloaded and printed 10/1/01.
	F	Internet web pages from the "iSCSIhba.com" website that mentions QLogic HBAs including the "SANblade 2300 Series", 8 pages, downloaded and printed 10/1/01.
	G	Internet pages from IReady News Archives entitled "iReady Rounding Out management team with two key executives," 2 pages, (printed 11/28/98).
	H	Toshiba, "Toshiba Delivers First Chips to Make Consumer Devices Internet-Ready Based on iReady's Design", 3 pages, Press release 10/14/98, downloaded 11/28/98.
	I	Internet pages entitled "iReady Products" from website http://www.ireadyco.com/products.html , 2 pages, printed 11/25/98.
	J	Iready News Archives. Toshiba, iReady shipping Internet Chip, 1 page, printed 11/28/98.
	K	Internet site www.interprophet.com , 17 pages, printed 3/1/00.
	L	The I-1000 Internet Tuner Features, iReady Corporation, 2 pages, date unknown.
	M	Internet pages entitled: Northridge/Southbridge vs. Intel Hub Architecture, 4 pages, printed 2/19/01.
	N	Gigabit Ethernet Technical Brief, Achieving End-to-End Performance. Alteon Networks, Inc., First Edition, 15 pages, September 1996.
	O	Richard Stevens, "TCP/IP Illustrated, Volume 1, The Protocols", pages 325-326 (1994).
	P	VT8501 Apollo MVP4 Documentation, VIA Technologies, Inc., pages i-iv, 1-11, cover and copyright page, 21 pages, revision 1.3 (Feb. 1, 2000).
	Q	Internet pages entitled: Technical White Paper-Xpoint's Disk to LAN Acceleration Solution for Windows NT Server, 14 pages, printed 6/5/97.
	R	Jato Technologies Internet pages entitled: Network Accelerator Chip Architecture, twelve-slice presentation, 13 pages, printed 8/19/98.
	S	EETIMES article entitled: Enterprise System Uses Flexible spec, dated August 10, 1998, 3 pages, printed 11/25/98.
	T	Internet pages entitled: Smart Ethernet Network Interface Cards which Berend Ozceri is developing, 2 pages, printed 11/25/98.
	U	Internet pages of Xaqti corporation entitled: Giga Power Protocol Processor Product review, 4 pages, printed 11/25/99.
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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
	A	Internet pages from website http://www.ireadyco.com/about.html , 3 pages, downloaded 11/2/98.
	B	IReady News Archives, "Revolutionary Approach to Consumer Electronics Internet Connectivity Funded", San Jose, CA. 2 pages, 11/20/97, downloaded and printed 11/02/98.
	C	IReady News Archives, "Seiko Instruments Inc. (SII) Introduces World's First Internet-Ready Intelligent LCD Modules Based on IReady Technology", 2 pages 10/26/98, downloaded and printed 11/2/98.
	D	NEWSwatch - IReady Internet Tuner to Web Enable Devices, November 5, 1996. 2 pages, printed 11/02/98.
	E	David Lammers, EETimes, 06/13/97, "Tuner for Toshiba, Toshiba taps iReady for Internet tuner," 2 pages, printed 11/02/98.
	F	Internet pages entitled: Comparison of Novell Netware and TCP/IP Protocol Architectures, by Janique S. Carbone, 19 pages, July 16, 1995, downloaded and printed 4/10/98.
	G	Internet pages entitled : Hardware Assisted Protocol Processing, which Eugene Feinberg is working on, 1 page, printed 11/25/98.
	H	Internet pages of Xpoint Technologies www.xpoint.com web site, 5 pages, printed 12/19/97.
	I	Internet pages entitled: Asante and 100BASE-T Fast Ethernet, 7 pages, printed 5/27/97.
	J	Internet pages entitled: A Guide to the Paragon XP/S-A7 Supercomputer at Indiana University, 13 pages, printed 12/21/98.
	K	Internet pages directed to: Technical Brief on Alteon Ethernet Gigabit NIC Technology, www.alteon.com , 14 pages, printed 3/15/97.
	L	Internet pages entitled: DART: Fast Application level Networking via Data-copy Avoidance, by Robert J. Walsh, 25 pages, printed 6/3/99.
	M	Internet pages of InterProphet entitled: Frequently Asked Questions, by Lynne Jolitz, 4 pages, printed 6/14/99.
	N	Intenet pages entitled iREADY About Us and iREADY Products, 7 pages, printed 11/25/98.
	O	Andrew S. Tanenbaum, Computer Networks, Third Edition, 1996, ISBN 0-13-349945-6.
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U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	A	4,991,133	2/5/91	Davis et al.	364	900	
	B	5,163,131	11/10/92	Row et al.	395	200	
	C	5,212,778	5/18/93	Dally et al.	395	400	
	D	5,280,477	1/18/94	Trapp	370	85.1	
	E	5,289,580	2/22/94	Latif et al.	395	275	
	F	5,303,344	4/12/94	Yokoyama et al.	395	200	
	G	5,412,782	5/2/95	Hausman et al.	395	250	
	H	5,448,566	9/5/95	Richter et al.	370	94.1	
	I	5,485,579	1/16/96	Hitz et al.	395	200.12	
	J	5,506,966	4/9/96	Ban	395	250	
	K	5,511,169	4/23/96	Suda	395	280	

Foreign Patent Documents

Translation

		Document Number	Date	Country	Class	Subclass	Yes	No
	L	WO/98/19412 PCT/US97/17257	9/26/97	International				
	M	WO/98/50852 PCT/US98/0871	11/12/98	International				
	N	WO/99/04343 PCT/US98/14729	7/17/98	International				
	O							

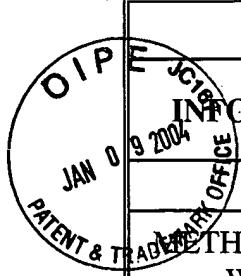
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*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate	
	A	5,548,730	8/20/96	Young et al.	395	280		
	B	5,558,121	12/24/96	Reddin et al.	395	200.15		
	C	5,590,328	12/31/96	Seno et al.	395	675		
	D	5,592,622	1/7/97	Isfel et al.	395	200.02		
	E	5,634,099	5/27/97	Andrews et al.	395	200.07		
	F	5,634,127	5/27/97	Cloud et al.	395	680		
	G	5,642,482	6/24/97	Pardillos	395	200.2		
	H	5,671,355	9/23/97	Collins	395	200.2		
	I	5,692,130	11/25/97	Shobu et al.	395	200.12		
	J	5,699,317	12/16/97	Sartore et al.	395	230.06		
	K	5,701,434	12/23/97	Nakagawa	395	484		
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		Document Number	Date	Country	Class	Subclass	Yes	No
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	M							
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*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	A	5,749,095	5/5/98	Hagersten	711	141	
	B	5,751,715	5/12/98	Chan et al.	370	455	
	C	5,752,078	5/12/98	Delp et al.	395	827	
	D	5,758,084	5/26/98	Silverstein et al.	395	200.58	
	E	5,758,089	5/26/98	Gentry et al.	395	200.64	
	F	5,758,186	5/26/98	Hamilton et al.	395	831	
	G	5,758,194	5/26/98	Kuzma	395	886	
	H	5,790,804	8/4/98	Osborne	395	200.75	
	I	5,812,775	9/22/98	Van Seters et al.	395	200.43	
	J	5,566,170	10/15/96	Bakke et al.	370	60	
	K	5,878,225	3/2/99	Bilansky et al.	395	200.57	
	L	5,930,830	7/27/99	Mendelson et al.	711	171	
	M	5,991,299	11/23/99	Radogna et al.	370	392	
	N	6,061,368	5/9/00	Hitzelberger	370	537	
	O	6,034,963	3/7/00	Minami et al.	370	401	
	P	5,678,060	10/14/97	Yokoyama et al.	395	831	
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	A	4,366,538	12/28/82	Johnson et al.	364	200	10/31/80
	B	5,588,121	12/24/96	Reddin et al.	395	200.15	05/09/96
	C	5,701,516	12/23/97	Cheng et al.	395	842	01/19/96
	D	5,809,328	09/15/98	Nogales et al.	395	825	12/21/95
	E	5,931,918	08/03/99	Row et al.	709	300	07/30/97
	F	5,935,205	08/10/99	Murayama et al.	709	216	06/21/96
	G	5,937,169	08/10/99	Connery et al.	395	200.8	10/29/97
	H	5,941,969	08/24/99	Ram et al.	710	128	10/22/97
	I	5,941,972	08/24/99	Hoeze et al.	710	129	12/31/97
	J	5,950,203	09/07/99	Stakuis et al.	707	10	12/31/97
	K	6,005,849	12/21/99	Roach et al.	370	276	09/24/97
	L	6,016,513	01/18/00	Glen H. Lowe	709	250	02/19/98
	M	6,026,452	02/15/00	William Michael Pitts	710	56	08/31/98
	N	6,044,438	03/28/00	Howard Thomas Olnowich	711	130	07/10/97
	O	6,047,356	04/04/00	Anderson et al.	711	129	04/18/94
	P	6,057,863	05/02/00	Sompong P. Olarig	345	520	10/31/97
	Q	6,065,096	05/16/00	Day et al.	711	114	09/30/97
	R	6,141,705	10/31/00	Anand et al.	710	15	06/12/98
	S	6,246,683	06/12/01	Connery et al.	370	392	05/01/98
	T	6,226,680	05/01/01	Boucher et al.	709	230	04/27/98
	U	6,247,060	06/12/01	Boucher et al.	709	238	11/12/99
	V	08/964,304		Napolitano et al.			11/04/97

Foreign Patent Documents

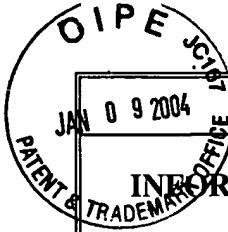
Translation

		Document Number	Date	Country	Class	Subclass	Yes	No
	W	WO 01/40960 A1	07/06/01	International				

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
	A	"Two-Way TCP Traffic over Rate Controlled Channels: Effects and Analysis," by Lampros Kalampoukas, Anujan Varma and K.K. Ramakrishnan, IEEE Transactions on Networking, Volume 6, Number 6, 17 pages, December 1998.
	B	"Toshiba Delivers First Chips to Make Consumer Devices Internet-Ready Based on iReady Design," IReady News Archives, 3 pages, downloaded November 2, 1998.
	C	"File System Design for an NFS File Server Appliance," by Dave Hitz, James Lau and Michael Malcolm, 12 pages, Copyright 1996.
	D	Adaptec press release, "Adaptec Announces EtherStorage Technology", 2 pages, June 14, 2000.
	E	Adaptec article entitled, "EtherStorage Frequently Asked Questions," 5 pages, July 19, 2000.
	F	Adaptec article entitled, "EtherStorage White Paper," 7 pages, July 19, 2000.
	G	Article entitled, "Computers; Storage," by James Berlino and Ananda Baruah, CIBC World Markets Equity Research, 9 pages, August 7, 2000.
	H	Merrill Lynch article entitled, "Storage Futures," by Steven Milunovich, 22 pages, May 10, 2000.
	I	Market Watch – News Articles, "Montreal start-up battles data storage bottleneck," by Susan Taylor, 2 pages, March 5, 2000.
	J	Internet-Draft "SCSI/TCP (SCSI over TCP)," by J.Satran et al., 38 pages, February 2000.
	K	
	L	
	M	
	N	
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	A	6,345,301	02/05/02	Burns et al.	709	230	
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*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	A	5,996,024	11/30/99	Blumenau	709	301	
	B	6,173,333	07/18/97	Jolitz et al.			
	C	2001/0025315A1		Jolitz			01/10/01
	D	2001/0004354A1		Jolitz			01/10/01
	E	5,058,110	10/15/91	Beach et al.	370	85.6	
	F	6,021,446	02/01/00	Gentry et al.	709	303	
	G	6,356,951	03/12/02	Gentry et al.	709	250	
	H	6,389,468	05/14/02	Muller et al.	709	226	
	I	6,434,651	08/13/02	Gentry, Jr.	710	260	
	J	6,427,169	07/30/02	Elzur	709	224	
	K	6,449,656	09/10/02	Elzur et al.	709	236	
	L	6,453,360	09/17/02	Muller et al.	709	250	

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	M	Article entitled "LRP: A New Network Subsystem Architecture for Server Systems", by P. Druschel and G. Banga, Rice University, October 1996, 15 pages.
	N	Internet RFC/STD/FYI/BCP Archives article with heading "RFC2140" entitled "TCP Control Block Interdependence", web address http://www.faqs.org/rfcs/rfc2140.html , 9 pages, printed 9/20/02.
	O	
	P	
	Q	
	R	
	S	
	T	

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